

What is claimed is:

1. A method for electrically interconnecting two printed circuit boards, comprising the steps of:

providing a first printed circuit board;

providing a second printed circuit board;

providing a receiving slot in one of the first and the second printed circuit boards such that the first and the second printed circuit boards are orthogonally intersected with each other; and

providing at least one electrical connector adjacent the receiving slot and in electrical connection with the first and the second printed circuit boards.

2. The method as recited in claim 1, wherein the first and the second printed circuit boards together define four quadrants.

3. The method as recited in claim 2, wherein the at least one electrical connector comprises a first connector arranged in a first quadrant and a second connector arranged in a fourth quadrant.

4. The method as recited in claim 3, wherein the first and the second connectors are mounted on the second printed circuit board.

5. The method as recited in claim 4, wherein the first and the second electrical connectors each comprise an electrical contact and an actuator capable of actuating the contact to have a wiping contact with the first and the second printed circuit boards.

6. A method for electrically interconnecting multiple printed circuit boards,

comprising the steps of:

providing a plurality of first printed circuit boards;

providing a plurality of second printed circuit boards;

providing receiving slots in either the first printed circuit boards or the second printed circuit boards such that the first and the second printed circuit boards are orthogonally intersected with each other, every two orthogonally arranged printed circuit boards together defining four quadrants; and

providing, in at least one of the four quadrants of every two orthogonally arranged printed circuit boards, a respective electrical connector to electrically interconnect the first and the second printed circuit boards.

7. The method as recited in claim 6, wherein the receiving slot is defined in the first printed circuit board.

8. The method as recited in claim 7, wherein the first and the second quadrants of every two orthogonally arranged printed circuit boards each have the electrical connector arranged therein, and each connector is mounted on the second printed circuit board.

9. The method as recited in claim 8, wherein the connectors respectively arranged in the first and the second quadrants are mirror image with respect to the second printed circuit board.

10. The method as recited in claim 9, wherein the third and the fourth quadrants of every two orthogonally arranged printed circuit boards each have the electrical connector arranged therein.

11. The method as recited in claim 10, wherein the connectors respectively arranged in the third and the fourth quadrants are mirror image with respect to the second printed circuit board.

12. The method as recited in claim 11, wherein the connectors respectively arranged in the first and the fourth quadrants are mirror image with respect to the first printed circuit board.

13. A method for configuring an electrical system adapted for mating with a complementary device, comprising the steps of:

- providing a printed circuit board having a surface;
- providing a first group of conductive traces on the surface;
- providing a second group of conductive traces on the surface and spaced from the first conductive traces;
- mounting a first electrical connector on the first group of the conductive traces, the first electrical connector defining a mating face; and
- mounting a second electrical connector on the second group of the conductive traces, the second electrical connector defining a second mating face facing the first mating face.

14. A method of making an interconnection system, comprising steps of:

- providing a first set of parallel spaced printed circuit boards defining first front edge sections thereof, respectively;
- providing a second set of parallel spaced printed circuit boards defining second front edge sections thereof, respectively; and
- intersecting each of said first set of parallel spaced printed circuit boards with all of said second set of parallel spaced printed circuit boards, respectively, around

the first front edge section of said each of the first set of parallel spaced printed circuit boards and the second front edge sections of said second set of parallel spaced printed circuit boards.

15. The method as recited in claim 14, further including a step of providing at least one electrical connector located in one of four quadrants derived from intersection by said each of said first set of parallel spaced printed circuit boards and the corresponding one of said second set of parallel spaced printed circuit boards, and electrically connected to said each of said first set of parallel spaced printed circuit boards and the corresponding one of said second set of parallel spaced printed circuit boards.

16. The method as recited in claim 15, wherein said connector extends in a longitudinal direction with a plurality of juxtaposed contacts therein, and said longitudinal direction is parallel to a center line defined by said four quadrants.